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Mark Fontenot

February 2002 **Journal of Computing Sciences in Colleges**, Volume 17 Issue 3

Publisher: Consortium for Computing Sciences in Colleges

Full text available: pdf(48.71 KB) Additional Information: [full citation](#), [references](#), [index terms](#)2 [VLSI architecture for lossless compression of medical images using the discrete wavelet transform](#)

I. Urriza, J. I. Artigas, J. I. García, L. A. Barragán, D. Navarro

February 1998 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: IEEE Computer Society

Full text available: pdf(58.57 KB)

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This paper presents a VLSI Architecture to implement the forward and inverse 2-D Discrete Wavelet Transform (FDWT/IDWT), to compress medical images for storage and retrieval. Lossless compression is usually required in the medical image field. The word length required for lossless compression makes too expensive the area cost of the architectures that appear in the literature. Thus, there is a clear need for designing an architecture to implement the lossless compression of medical images using ...

Keywords: Medical Image compression, VLSI architectures, DWT3 [Session 7D: Issues in substrate coupling: Highly accurate fast methods for extraction and sparsification of substrate coupling based on low-rank approximation](#)

Joe Kanapka, Jacob White

November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Press

Full text available: pdf(637.11 KB)

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More aggressive design practices have created renewed interest in techniques for analyzing substrate coupling problems. Most previous work has focused primarily on faster techniques for extracting coupling resistances, but has offered little help for reducing the resulting resistance matrix, whose number of nonzero entries grows quadratically with the number of contacts. Wavelet-like methods have been applied to sparsifying the resistance matrix representing the substrate coupling, but the accur ...



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Signal Processing and Its Applications, 2003. Proceedings. Seventh International Symposium on
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- ☐ 2. **Efficient direct 2D architecture for lifted biorthogonal DWT**
 Alam, M.; Badawy, W.; Dimitrov, V.; Jullien, G.;
Signal Processing Systems, 2003. SIPS 2003. IEEE Workshop on
 27-29 Aug. 2003 Page(s):340 - 345
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- ☐ 3. **A low-cost VLSI architecture design for non-separable 2-D discrete wavelet transform**
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